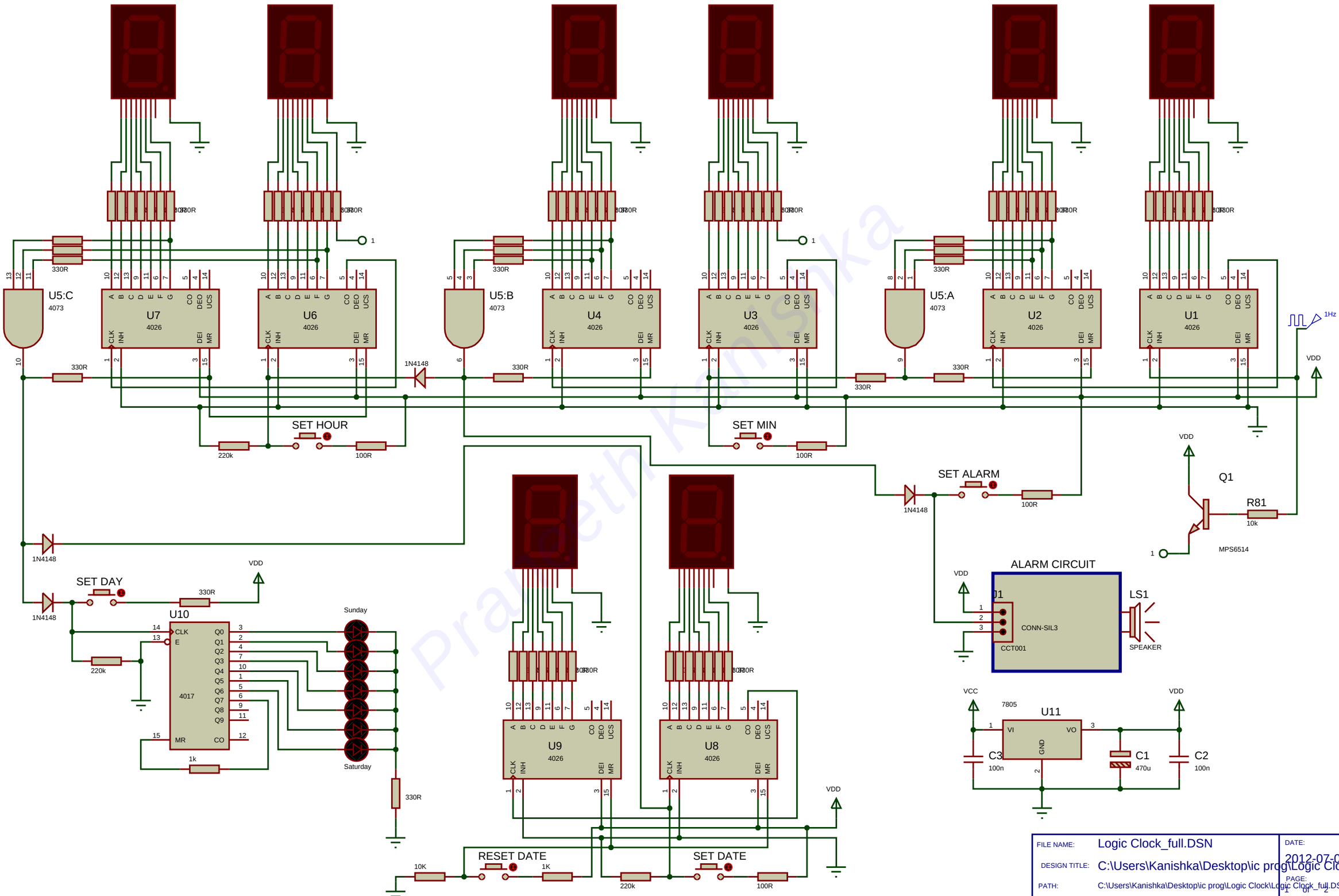

U5:C
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U5:B
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U7
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U2
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U1
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U9
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U8
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U10
4017
330R
1N4148
220k
100R
10K
1K
1k
SET HOUR
SET MIN
SET ALARM
SET DAY
RESET DATE
SET DATE
Sunday
Saturday
1Hz
VDD
Q1
MPS6514
R81
10k
ALARM CIRCUIT
CONN-SIL3
CCT001
LS1
SPEAKER
VCC
7805
U11
C1
470u
C2
100n
C3
100n
FILE NAME: Logic Clock_full.DSN
DESIGN TITLE: C:\Users\Kanishka\Desktop\ic prog\Logic Clock\Logic C
PATH: C:\Users\Kanishka\Desktop\ic prog\Logic Clock\Logic Clock_full.DSN
BY: <NONE>
REV:<NONE>
DATE: 2012-07-08
PAGE: 1 of 2
TIME: 11:06:57 ??.?.